

REMARKS

Claims 1, 3 to 20, 22 to 25, 27 to 32 and 34 to 41 are pending in this application of which claims 1, 10, 18, 25 and 32 are the independent claims. Favorable reconsideration and further examination are respectfully requested.

Claims 1, 10, 18, 20, 25 and 32 were rejected under 35 U.S.C. §112, first paragraph and second paragraph. Based on the foregoing claim amendments, Applicants request withdrawal of the §112 rejections.

Claims 1, 4 to 6, 8 to 20, 23 to 25 and 27 to 32 were rejected under 35 U.S.C. § 103(a) as being obvious over Ohta et al. (U.S. Patent Application Publication Number 2002/0083317 hereinafter "Ohta") in view of Tardo et al. (U.S. Patent Number 7,082,534).

Claim 1 is directed to a processor. The processor includes a crypto unit including a cipher core configured to cipher data received, authentication cores configured to authenticate the ciphered data and processing contexts each configured to process at least one data packet at a time and to store cipher keys and algorithm context associated processing the at least one data packet. At least two authentication cores each implements a different authentication algorithm. The processor also includes an authentication buffer configured to store the ciphered data and provide the ciphered data to the authentication cores each in an amount based on the corresponding authentication algorithm implemented. Each processing context includes authentication of the at least one packet. The authentication buffer includes buffer elements.

Each buffer element corresponds to a respective one of the plurality of processing contexts and a number of the plurality of processing contexts does not equal a number of the authentication cores.

The applied art is not understood to disclose or to suggest the foregoing features of claim 1. In particular, the Ohta reference does not disclose or suggest that that each processing context includes authentication of at least one packet, that each buffer element corresponds to a respective one of the plurality of processing contexts and that a number of the plurality of processing contexts does not equal a number of the authentication cores.

Independent claims 10, 18, 20, 25 and 32 include the feature that that each processing context includes authentication of at least one packet, that each buffer element corresponds to a respective one of the plurality of processing contexts and that a number of the plurality of processing contexts does not equal a number of the authentication cores as recited in claim 1. Applicants submit that the Ohta reference should also be withdrawn with respect to claims 10, 18, 25 and 32 for at least the same reasons as claim 1.

Applicants further note that with respect to claims 18, 20, 25 and 32 that the cited prior art does not teach the limitation of an integrated circuit chip.

Moreover, with respect to claims 36 to 38, the cited art does not teach that the number of the plurality of processing contexts does not equal a number of the plurality of cipher cores.

For at least the foregoing reasons, Applicants request withdrawal of the art rejections.

Applicants submit that all dependent claims now depend on allowable independent claims.

It is believed that all of the pending claims have been addressed. However, the absence of a reply to a specific rejection, issue or comment does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above may not be exhaustive, there may be reasons for withdrawing the prior art cited with regards to any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as intent to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

Applicants submit that the entire application is now in condition for allowance. Such action is respectfully requested at the Examiner's earliest convenience.

All correspondence should be directed to the address below. Applicants' attorney can be reached by telephone at (781) 401-9988 ext. 123.

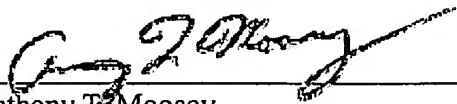
Applicants : Sydir et al.
Serial No. : 10/749,913
Filed : December 29, 2003
Page : 18 of 18

Attorney's Docket No.: INTEL-013PUS
Intel Docket No. P17940

No fee is believed to be due for this Response; however, if any fees are due, please apply such fees to Deposit Account No. 50-0845 referencing Attorney Docket: INTEL-013PUS.

Respectfully submitted,

Date: June 9, 2008



Anthony T. Moosey
Reg. No. 55,773

Attorneys for Intel Corporation
Daly, Crowley, Mofford & Durkee, LLP
354A Turnpike Street - Suite 301A
Canton, MA 02021-2714
Telephone: (781) 401-9988 ext. 123
Facsimile: (781) 401-9966